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## \*BIBDATASHEET\*

CONFIRMATION NO. 7379

Bib Data Sheet

SERIAL NUMBER 10/600,740	FILING DATE 06/20/2003 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. P8987
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## APPLICANTS

Umesh Nair, Newark, CA;

None PD

\*\* CONTINUING DATA \*\*\*\*\*

None PD

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 09/24/2003

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met/after	CA	6	24	3
Verified and Acknowledged	<i>Paul Jinkins</i> PD Examiner's Signature	Initials			

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## TITLE

System for optimizing buffers in integrated circuit design timing fixes

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____
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